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Job offer: Layout Engineer

We are **ITSELECTA**, a recruiting agency based in Krakow, Poland. Our mission is to find the right people for our customer and the perfect job for our candidates. We are focused on recruiting for customers in Poland and Czech Republic.

Our customer is a well-known international company in the electronic field and we are selecting candidates to join their team in the Prague office. It is a great chance to join a great company if you have the right skills and motivation to move in the beautiful city of Prague.

Responsibilities:

- The offered position is in the field of integrated circuit layout
- Translation of schematic design into full custom layout in compliance with design rules
- Verifications of layout to ensure all design requirements are met
- Performing of floor planning for optimal die area
- Generation of wire bonding diagram for assembly approval before the tape-out

Requirements:

- Diploma in Electrical Engineering (with Microelectronics equivalent)
- Good knowledge of CMOS/BiCMOS process to support the Design Engineers to translate the schematic into mask layout
- Ability to generate full custom layout using Cadence Virtuoso Layout Editor
- Ability to perform layout verifications (DRC, ERC, LVS and XOR) using Mentor Graphics Calibre
- Basic knowledge of Linux/UNIX operating system
- For junior positions the candidate can be fresh graduate
- For senior positions, minimum of 5 years of related working experience is required; the candidate must be excellent in IC Layout knowledge with the capability to co-ordinate and supervise the project till completion with good quality for the tape-out

The company offer:

- Attractive salary and wide range of corporate benefits
- Great opportunity to learn new things
- Good team spirit and friendly working environment
- Office located in Prague city center
 - Possibility to travel abroad
 - Allocation package
 - Bonus: up to 30%, strictly related to the results

- Permanent contract

Apply sending your CV at: info@itselecta.com. Email subject: Engineer for Microcontroller Applications

Deadline: 30/04/2017

